

Form 1449*	Atty. Docket No.: 884.279US1	Serial No. Unknown 09/607,421
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
AB	Bryant, R.E., "A Switch-level Model and Simulator for MOS Digital Systems", <u>IEEE Transactions on Computers</u> , C-33, 2, 160-177, (Feb. 1984) ✓
AB	Bryant, R.E., "Algorithmic Aspects of Symbolic Switch Network Analysis", <u>IEEE Transactions on Computer-Aided Design</u> , Vol. CAD-6, 618-633, (July 1987)
AB	Bryant, R.E., "Boolean Analysis of MOS Circuits", <u>IEEE Transactions on Computer-Aided Design</u> , Vol. CAD-6, 634-649, (July 1987)

Examiner <u>Samuel Broda</u>	Date Considered <u>11 March 2004</u>
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*Substitute Disclosure Statement Form (PTO-1449)

**EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.